

In the Specification

Please substitute the following amended paragraph for the paragraph beginning on page 10, line 8:

Referring now to FIGS. 8a, 8b and 8c, conductive control gates, regions 38 and 40, are shown, which preferably are polysilicon control gates. That portion of the control gate, 38, in the programming bit line channel is entirely disposed over the wider portion of the floating gate. As shown in FIG. 8b the floating gate and control gate in the programming bit line channel are in a "stacked gate" like configuration. In the reading bit line channel, the floating gate and control gate are in "split gate" like configuration, as shown in FIG. 8c, which is the same as for traditional memory cells. In preferred embodiments of the invention the control gate of the reading bit line channel is wider than the control gate of the programming bit line channel. The conductive control gates being parallel conductive lines are appropriate for and are utilized as word lines. At this stage the positions of drain regions, 14, between adjacent control gates, are delineated and the drain regions are now readily formed. As well, referring to FIG. 8a, a source region 14 is disposed in the substrate 6 and adjacent to a first side of the floating gate 34. The isolation region divides the drain region 10 adjacent to the second side of the floating gate 34 into a first drain region 10a and a second drain region 10b, wherein width of a portion of the floating gate 34 near the first drain region 10a is smaller than another portion of the floating gate 34 near the second drain region 10b, and width of a portion of the control gate 38 near the first drain region 10a is larger than another portion of the control gate near the second drain region 10b. The next stage of the process is shown in FIGS. 9a, 9b and 9c.

In the Drawings

FIGs. 8a, 8c, 9a, 9c and 10b are amended to correct the informalities of symbols, in which the incorrect symbols about the source and drain region are amended. Specifically, in FIG 8a, reference numbers 10b and 14 are added, and arrowheads for reference characters 8b and 8c are added. In FIG. 8c, reference number "14" is changed to "10" and reference number "10" is changed to "14."

In FIG 9a, reference numbers 10b and 14 are added, and arrowheads for reference characters 9b and 9c are added. In FIG. 9c, reference number "14" is changed to "10" and reference number "10" is changed to "14."

In FIG. 10b, reference number "14" is changed to "10" and reference number "10" is changed to "14."

Attachments

Annotated Sheets (three)

Replacement Sheet (three)